

Amendments to the Claims:

This listing of claims replaces all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) An interface in a Radio Base Station for transmission and reception of user data to and from one or more user equipments in a radio communication network, comprising:
a plurality of links having a ~~minimised~~ minimized bandwidth for carrying data independent of the functionality of the radio access network and the airborne radio transmission; and,
one or more user data links for uplink and downlink, a control and supervision link, and a synchronization link.
2. (Cancelled).
3. (Currently Amended) The interface according to claim 1, ~~intended for carrying~~ wherein said interface carries baseband signals comprising digital signal components that describe the airborne signal.
4. (Currently Amended) The interface according to claim 3, wherein the user data link ~~transfers~~ transfers the downlink user data as symbols and the uplink user data as sampled symbols.
5. (Currently Amended) The interface according to claim ~~[[2]]~~ 1, wherein the user data link carries information about stream identity for routing and/or supervision.
6. (Currently Amended) The interface according to claim ~~[[2]]~~ 1, wherein the control and supervision link is split between a processor based link and fast indications.

7. (Currently Amended) The interface according to claim 6₁ wherein the fast indications are used to determine the status of the radio transmission part when the processor based link has failed.

8. (Currently Amended) The interface according to claim 6₁ wherein an indication is used to reset the radio transmission part.

9. (Currently Amended) The interface according to claim [[2]] 1₁ wherein the ~~synchronisation~~ synchronization link is used to control the transmission time of the user data link.

10. (Currently Amended) The interface according to claim [[2]] 1₁ wherein the ~~synchronisation~~ synchronization link is used to time stamp the reception time of the user data link.

11. (Currently Amended) The interface according to claim 6₁ wherein a hardware reset is encoded in the processor based link layer 1 protocol as a code violation.

12. (Currently Amended) The interface according to claim 5₁ wherein transmission of parity bits is suspended during stream identity transmission.

13. (Currently Amended) The interface according to claim 4₁ wherein the uplink data format consists of a fast changing mantissa and a slow changing exponent.

14-15. (Cancelled)

16. (Currently Amended) The interface according to claim [[2]] 1₁ wherein the uplink interface ~~serialiser~~ serializer is controlled by the ~~synchronization~~ synchronization link.